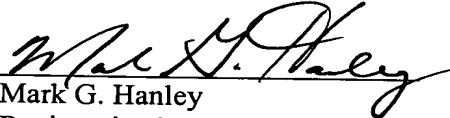


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Rose et al.
U.S. Serial No. : 10/027,053
Filed : December 20, 2001
Title : BIT ERROR RATE TEST
SYSTEM FOR MULTI-
SOURCE AGREEMENT
COMPLIANT
TRANSCIVERS
Assignee : Intel Corporation
Art Unit : 2133
Examiner : Joseph D. Torres

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) Amendments, Commissioner for
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) VA 22313-1450 on this date:

) Dated: August 29, 2005

) 
) Mark G. Hanley
) Registration No. 44,736
)

RESPONSE TO THE OFFICE ACTION DATED APRIL 28, 2005

Mail Stop Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following amendments and consider the following remarks.

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 4 of this paper.

An **Appendix** including previously filed corrected drawings and a transmittal used to submit the corrected drawings to the Patent Office is attached following page 6 of this paper.